

















SHARED PINS TABLE

NET BUS 1 BUS 2 ITAG SDC2 PB3 PH11 PH12 CAM 12C4 CAM I2C4 P15 P16 P17 PA4 PA6 PC2 PC3 CAM SAI2A CAM SAI2A CAM SAI2A CAM ANALOG ANALOG CAM ANALOG

SPI2

ANALOG

POWER NETS TABLE

NET	TYPE	RANGES	DESCRIPTION	
VIN VIN VCC VCC	PORTENTA INPUT PORTENTA OUTPUT	4.1V to 6V. 1.1V to 3.3V in steps, max 1A.	Default 3.3V, PMIC (U10) programmable output. Default 3.3V, PMIC (U10) programmable output.	
VSYS	PORTENTA RESERVED OUTPUT	RESERVED, DO NOT USE 3.5V to 4.2V, max 600mA.	Default 4.2V, PMIC (U10) programmable output which is also the input voltage of the bucks inside the PMIC itself.	
LICELL LICELL	PORTENTA INPUT	Coinc cell max 3.6V, max 46uA.	Max 4uA with PMIC (U10) in coin cell mode, max 46uA with PMIC in standby/suspend mode.	

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Title: HD Connectors female		<u> </u>	
ID: ASX00027	Revision: V1.5	(C)	BY SA
Date: 2022-01-24 Time: 12:53:11	Sheet 8 of 11		J. 0A



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File: HDConn_FEMALE.SchDoc Author: Silvio Navaretti RevAuthor: 6





