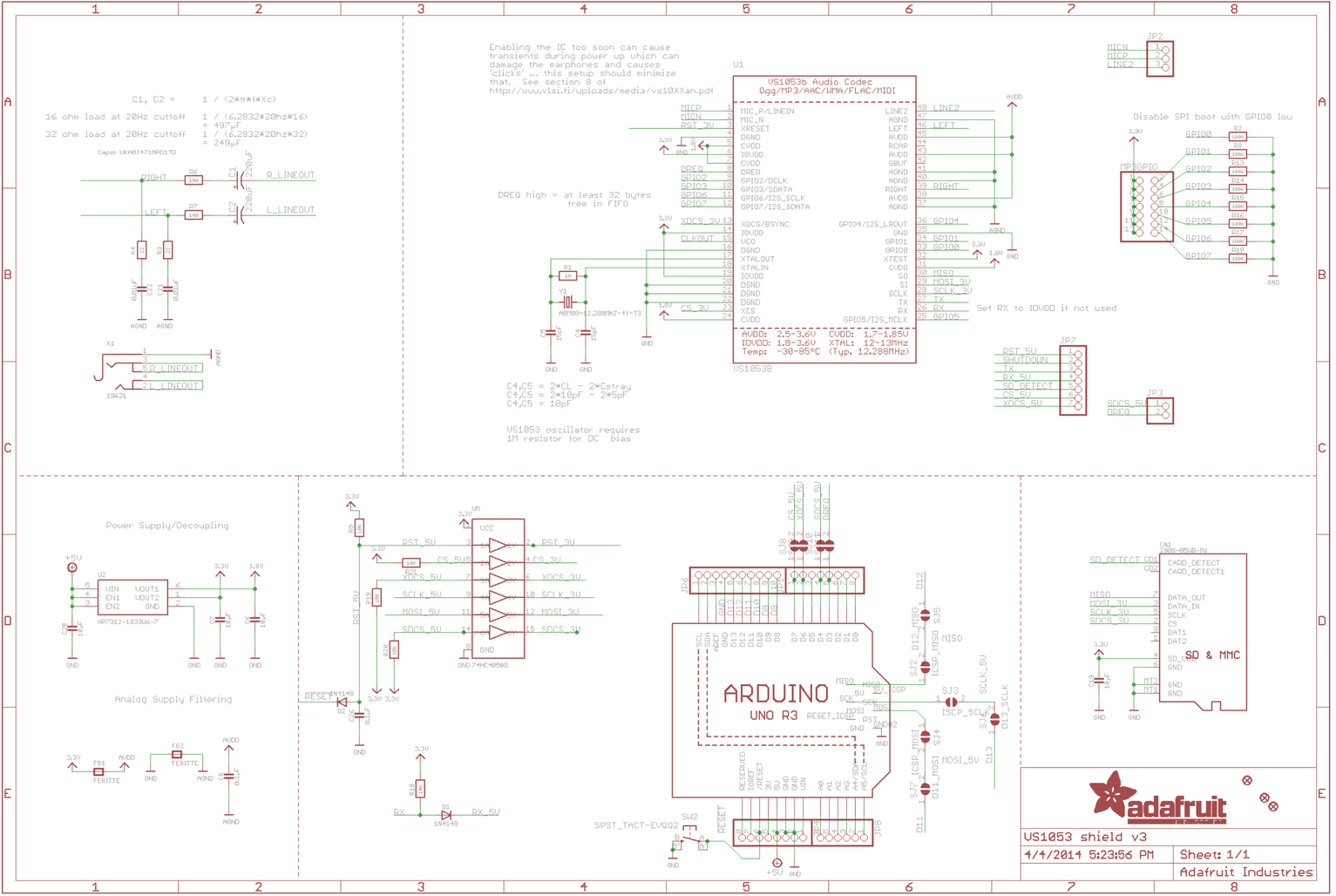
C1, C2 = 1 / (2*π*f*Xc)
16 ohm load at 20Hz cutoff 1 / (6.2832*20hz*16) = 497µF
32 ohm load at 20Hz cutoff 1 / (6.2832*20hz*32) = 249µF
Caps: UKA0J471MPD1TD
R_LINEOUT
L_LINEOUT
AGND
X1
19621
Enabling the IC too soon can cause transients during power up which can damage the earphones and causes 'clicks' ... this setup should minimize that. See section 8 of http://uuu.vlsi.fi/uploads/media/vs10XXan.pdf
U1
VS1053b Audio Codec
Ogg/MP3/AAC/WMA/FLAC/MIDI
MIC_P/LINEIN
MIC_N
XRESET
DGND
CVDD
IOVDD
DREQ
GPIO2/DCLK
GPIO3/SDATA
GPIO6/I2S_SCLK
GPIO7/I2S_SDATA
XDCS/BSYNC
IOVDD
VCO
DGND
XTALOUT
XTALIN
IOVDD
DGND
DGND
DGND
XCS
CVDD
LINE2
AGND
LEFT
AVDD
RCAP
AVDD
GBUF
AGND
AGND
RIGHT
AVDD
AGND
GPIO4/I2S_LROUT
GND
GPIO1
GPIO0
XTEST
CVDD
SO
SI
SCLK
TX
RX
GPIO5/I2S_MCLK
AVDD: 2.5-3.6V CVDD: 1.7-1.85V
IOVDD: 1.8-3.6V XTAL: 12-13MHz
Temp: -30-85°C (Typ. 12.288MHz)
VS1053B
DREQ high = at least 32 bytes free in FIFO
C4,C5 = 2*CL - 2*Cstray
C4,C5 = 2*10pF - 2*5pF
C4,C5 = 10pF
VS1053 oscillator requires 1M resistor for DC bias
Set RX to IOVDD if not used
Disable SPI boot with GPIO0 low
MP3GPIO
JP2
MICN
MICP
LINE2
JP7
RST_5U
SHUTDOWN
TX
RX_5U
SD_DETECT
CS_5U
XDCS_5U
JP3
SDCS_5U
DREQ
Power Supply/Decoupling
MCP1312-183346-7
Analog Supply Filtering
FERRITE
U5
74HC10500
RESET
1N4148
ARDUINO
UNO R3
SPST_TACT-EVQQ2
SD & MMC
CARD_DETECT
CARD_DETECT1
DATA_OUT
DATA_IN
SCLK
CS
DAT1
DAT2
SD_USE
GND
MT2
MT1
adafruit
VS1053 shield v3
4/4/2014 5:23:56 PM
Sheet: 1/1
Adafruit Industries
Place C1 as close as possible to PVDD.
Any resistance or inductance in the trace between the device and the capacitor can cause a loss in efficiency
The audio input pins (INR+/- and INL+/-) must run side-by-side to maximize common-mode noise cancellation.
U4
TPA2012D2
2.1W/CH Audio Amp
PVDD
PVDD
AVDD
NC
NC
INL-
INL+
SHUTDOWN_L
IND-
IND+
SHUTDOWN_R
THERMAL
PGND
PGND
AGND
OUTL-
OUTL+
OUTR-
OUTR+
Gain
G0
G1
G0 G1 V/V dB
0 0 2 6
0 1 4 12
1 0 8 18
1 1 16 24
PVDD/PVDD: 2.5-5.5V
Temp: -40-85°C
TS2012
LEFTSPK
RIGHTSPK
Note on speaker choice (4/8/16/32 ohm, etc.):
Using speakers more resistive than 4-Ω dramatically increases the thermal performance by reducing the output current and increasing the efficiency of the amplifier.
GAIN1 and GAIN0 have built-in pull down resistors. VS1053 out is 2Vpp so 2V/V gain keeps it under 4.5V for low distortion.